

UNIVERSITY OF CALICUT
Pareeksha Bhavan

No. EG 1/2/B Tech-2004 & 2K/2016

Calicut University P O.
673 635

Dated: 02-07-2018

NOTIFICATION

It is notified for the information of all concerned that the **III Semester B.Tech / PT B.Tech/B Arch (2K Scheme) Supplementary Examination December 2015** will be conducted by the University from **18th July 2018** onwards as per the following time table.

TIME TABLE FOR THE III SEMESTER B.TECH / PART TIME B.TECH
DEGREEE (2K SYLLABUS) SUPPLEMENTARY EXAMINATION
DECEMBER 2015

Time of Examination : 9.30 am to 12.30 pm

Maximum Marks : 100 each

Day and Date	Subject	
Wednesday 18 th July 2018	CH/AI/CE/EE/IC/ME/ PM/EC/PE/PT/BM/BT/ PTCE/PTEC/PTEE/ PTCH/PTME2K 301	Engineering Mathematics-III
	CS/IT/PTCS 2K 301	Engineering Mathematics-III
Thursday 19 th July 2018	CH/PTCH 2K302	Organic Chemistry
	ME/PM/PTME 2K302	Mechanics of Fluids
	EC2K302/PTEC2K302	Electric Circuits & Net Work Theory
	CS/IT/PTCS2K302	Data Structures & Algorithms
	CE/EE/PTCE2K302/PT 2K403	Mechanics of Solids
Friday 20 th July 2018	CH2K303/PTCH2K107	Basic Electronics
	ME/PM/PE/PTME2K303	Mechanics of Solids
	EC2K303	Solid State Devices
	CE2K 303	Building Technology- II
	AI2K 303	Analog Devices and Circuits-I
	CS/IT/PTCS2K303	Discrete Computational Structures
Monday 23 rd July 2018	CH2K304/PTCH2K108	Process Calculation
	CE/PTCE2K304	Electrical and Electronics Engineering
	ME/PM/PTME2K304	Material Science & Metallurgy
	CS/IT2K304/PTCSE2K1 07	Basic Electronics Engineering
	EE2K304/PTEE2K106	Electronics-I

Tuesday 24 th July 2018	CH2K305/PTCH2K304	Fluid & Particle Mechanics-I
	CE/PTCE2K305	Engineering Geology
	EC/AI2K305/PTEC 2K305	Digital Electronics
	CS/IT2K305/PTCSE2K304	Switching Theory & Logical Designs
	EE2K305/PTEE2K107	Electric Circuit Theory
	ME/PM2K305/PT2K406	Machine Drawing
Wednesday 25 th July 2018	CH2K306/PTCH2K305	Chemical Engineering Thermodynamics-I
	ME/PM2K306	Electrical Engineering-II
	EE 2K 306	Electrical Measurement & Measuring Instruments
	EC/AI/PT2K306	Electrical Engineering
	CS/IT2K306/PTCS2K305	Electric Circuits & systems

TIME TABLE FOR III SEMESTER B.ARCH (2K) SUPPLEMENTARY
EXAMINATION DECEMBER 2015

Time of Examination : 9.30 a.m to 12.30 p.m

Maximum Marks : 100 each

Wednesday 18 th July 2018	AR 2K 301	Statistics
Thursday 19 th July 2018	AR2K302	Theory of Structures-I

Sd/-
CONTROLLER OF EXAMINATIONS