

UNIVERSITY OF CALICUT
Pareeksha Bhavan

No. EG 1/2/B Tech/2k & 2004 scheme/2016

Calicut University.P.O.
673 635

Dated: 04-11-2017

NOTIFICATION

It is notified for the information of all concerned that the **IV Semester - 2004 Scheme (2004 to 2008 Admissions) B Tech / B Arch** Supplementary Examinations **June 2016** will be conducted by the University from **17-11-2017** onwards as per the schedule given below.

TIME TABLE FOR THE FOURTH SEMESTER - (2004 scheme) B.TECH / B Arch (2004 to 2008 Admn)
SUPPLEMENTARY EXAMINATIONS JUNE 2016

Time of Examination : 9.30 am to 12.30 pm Maximum Marks : 100 each

Day & Date		Subject
Friday 17 th November 2017	EN 04 401 (A)	Engineering Mathematics IV (Common for all branches except CS & IT)
	EN 04 401 (B)	Engineering Mathematics IV (Common for CS/IT 04 branches)
Monday 20 th November 2017	EN 04 402	Environmental Studies (Common for all branches)
Wednesday 22 nd November 2017	CH 04 403	Physical & Analytical Chemistry
	CE 04 403	Structural Mechanics – I
	ME 04 403	Thermodynamics
	EE 04 403	Electrical Measurements and Measuring Instruments
	EC 04 403	Digital Electronics
	CS/IT 04 403	Systems Programming
	AI/BM 04 403	Introduction to Microprocessors
	PE 04 403	Theory of Mechanism
	IC 04 403	Electrical Machines
	PT 04 403	Mechanics of Solids
AM 04 403	Theory of Machines	

Tuesday 28 th November 2017	CH 04 404	Fluid & Particle Mechanics – II
	CE 04 404	Fluids Mechanics
	ME/AM 04 404	Metallurgy and Material Science
	EE 04 404	Electronics II
	EC 04 404	Computer Organizations & Architecture
	CS/IT 04 404	Microprocessor Based Design
	AI 04 404	Analog Electronics & Pulse Circuits
	PE 04 404	Fluid Mechanics & Machinery
	PT 04 404	Electronics Circuits
	BM 04 404	Electronic Instrumentation
Thursday 30 th November 2017	CH 04 405	Chemical Engineering Thermodynamics-II
	CE 04 405	Building Architecture & Town Planning
	ME 04 405	Advanced Mechanics of solids
	EE 04 405	Electrical Machines – I
	EC 04 405	Electronics Circuits – II
	CS 04 405	Computer Organization & Design
	IT 04 405	Programming Paradigms
	AI 04 405	Electronic Instrumentation & Measurements
	PE 04 405	Thermal Engineering
	IC 04 405	Linear Integrated Circuits
	BM 04 405	Life Science – II
	AM 04 405	Auto Chasis
Tuesday 5 th December 2017	CH 04 406	Material Science
	CE 04 406	Engineering Geology
	ME/AM 04 406	Fluid Machinery
	EE 04 406	Linear Systems Analysis
	EC 04 406	Analog Communications

Tuesday 5 th December 2017	CS 04 406	Electronic Circuits and Systems
	IT 04 406	Communication Systems
	AI/BM 04 406	Linear Integrated Circuits and Applications
	PE 04 406	Electronics and Microprocessors
	IC 04 406	Microprocessors and Micro Controllers
	PT 04 406	Machine Drawing
Thursday 7 th December 2017	CE 04 407	Civil Engineering Drawing - I

TIME TABLE FOR THE FOURTH SEMESTER B.ARCH (2004 SCHEME)
SUPPLEMENTARY EXAMINATIONS JUNE 2016

Time of Examinations: 9.30 am to 12.30 pm

Maximum Marks : 100 each

Day and date		Subject
Friday 17 th November 2017	AR 01 401	Theory of Structures - II
Monday 20 th November 2017	AR 01 402	Land Surveying
Wednesday 22 nd November 2017	AR 01 403	Building Construction-III
Tuesday 28 th November 2017	AR 01 404	History of Architecture -III
Thursday 30 th November 2017	AR 01 406	Theory of Design -II

Sd/-

CONTROLLER OF EXAMINATIONS